

### REMARKS

Claims 1-16, 20, 21, 26-72, 75, 76, 79, 80, 84, 85, 90 and 91 are pending, with claims 1-4, 8, 9, 20 and 21 being independent. Claims 4, 5, 8, 9, 20 and 21 have been amended. In particular, claims 4 and 5 have been amended to require only a single power supply line and at least one pixel, instead of multiple power supply lines and pixels; claims 8 and 9 have been similarly amended to require only a single power supply line, a single pixel and single source signal line (claim 8) or gate signal line (claim 9); claim 20 has been amended to recite that an operation of a picture signal processing circuit is stopped while the electroluminescence display device carries out a display by using the analog signals; and claim 21 has been amended to recite that a data readout operation of the VRAM is stopped while the electroluminescence display device carries out a display by using the analog signals. Support for the amendments to claims 4, 5, 8 and 9 may be found in the application at Fig. 37 (claims 4 and 5), Fig. 24 (claim 8) and Fig. 6 (claim 9), and support for the amendments to claims 20 and 21 may be found in the application at pages 19-20. The specification has been amended at page 20 to correct minor typographical errors. No new matter has been introduced.

Applicant acknowledges with appreciation the Examiner's allowance of claims 1-16, 26-72, 75, 76, 79, 80, 84, 85, 90 and 91.

Claims 20 and 21 have been amended in response to the rejection under section 112, second paragraph. The amendments are believed to address the Examiner's concerns and, accordingly, applicant requests reconsideration and withdrawal of this rejection.

Applicant : Shunpei Yamazaki et al.  
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Respectfully submitted,

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Barbara A. Benoit for Barbara Benoit  
John F. Hayden Reg. No. 54,777  
Reg. No. 37,640

Customer No. 26171  
Fish & Richardson P.C.  
1425 K Street, N.W., 11th Floor  
Washington, DC 20005-3500  
Telephone: (202) 783-5070  
Facsimile: (202) 783-2331

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